



PATENT  
0717-0465P

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: Kiyoshi KUMATA, et al. Conf.: 3865  
Appl. No.: 09/846,297 Group: 2675  
Filed: May 2, 2001 Examiner: NGUYEN  
For: OMNIAZIMUTHAL VISUAL SYSTEM

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APR 16 2002

Technology Center 2600

LARGE ENTITY TRANSMITTAL FORM

Assistant Commissioner for Patents  
Washington, DC 20231

April 12, 2002

Sir:

Transmitted herewith is an amendment in the above-identified application.

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	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR		PRESENT EXTRA	RATE	ADDITIONAL FEE
TOTAL	9	-	20	=	0	\$18	\$0.00
INDEPENDENT	1	-	3	=	0	\$84	\$0.00
<input type="checkbox"/> FIRST PRESENTATION OF A MULTIPLE DEPENDENT CLAIM						\$280	\$0.00
						TOTAL	\$0.00

Petition for ( ) month(s) extension of time pursuant to 37 C.F.R. §§ 1.17 and 1.136(a). \$0.00 for the extension of time.

No fee is required.

Check(s) in the amount of \$0.00 is(are) enclosed.

Please charge Deposit Account No. 02-2448 in the amount of \$0.00. This form is submitted in triplicate.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

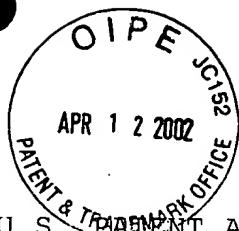
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0717-0465P

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ATTACHMENT

(Rev. 09/27/01)



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PATENT  
0717-0465P 05-08-02  
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Conf.: 3865 **RECEIVED**

Appl. No.: 09/846,297

Group: 2675 APR 16 2002

Filed: May 2, 2001

Examiner: NGUYEN Technology Center 2600

For: OMNIAZIMUTHAL VISUAL SYSTEM

REPLY UNDER 37 C.F.R. § 1.111

Assistant Commissioner for Patents  
Washington, DC 20231

April 12, 2002

Sir:

In reply to the Examiner's Office Action dated January 16, 2002, the following remarks are respectfully submitted in connection with the above-identified application.

IN THE CLAIMS:

Please add the following claims:

*Sub B1*  
--10. (New) The omniazimuthal visual system of claim 1, wherein the arithmetic/logic circuit is a panoramic transformation circuit which only requires changing one parameter in order to alternatively perform a pan function using the image data output from the imaging section.

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